

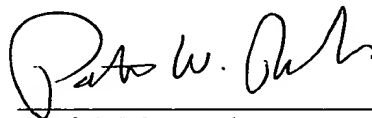
Express Mail No.: EL752245351US

30GF-9118
PATENT

Remarks

Please enter the foregoing preliminary amendment prior to examination of the present application. Claims 1, 6, 12, 18, and 23 have been cancelled. New Claims 24-31 have been added. Applicants submit that this Amendment adds no new matter.

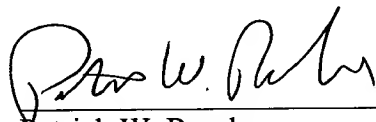
Respectfully Submitted,



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7. (once amended) An I/O module comprising:
- at least one connector pin; and
- a control circuit comprising a plurality of solid state switches, said solid state switches controlling a configuration of the at least one pin.
9. (once amended) An I/O module in accordance with Claim 8 [7] wherein an energization state of each said at least one port controlling a state of a respective at least one switch.

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30GF-9118
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gareis, et al.

Serial No.: 09/682,060

Filed: July 16, 2001

For: METHOD AND SYSTEM FOR
CONFIGURING
INPUT/OUTPUT POINTS

Art Unit: 2182

Examiner: Not yet assigned

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MAY 14 2002

SUBMISSION OF MARKED UP CLAIMS

Technology Center 2100

Hon Commissioner for Patents
Washington, D.C. 20231

Submitted herewith are marked up Claims in accordance with 37 C.F.R.
1.121(c)(1)(ii), wherein additions are underlined and deletions are [bracketed].

IN THE CLAIMS

2. (once amended) A control circuit for configuring at least one I/O module connector pin, said circuit comprising at least one port controlling a configuration of the at least one pin, [in accordance with Claim 1 wherein] said at least one port comprises at least one of a Pull-Down (PD) port, a Pull-Up (PU) port, a Discrete High (DH) port, a Discrete Low (DL) port, a positive 15 volt (P15V) port, a negative 15 volt (N15V) port, a range (RANGE) port, and a voltage out (VOUT) port.

3. (once amended) A control circuit for configuring at least one I/O module connector pin, said circuit comprising:

at least one port controlling a configuration of the at least one pin; and

[in accordance with Claim 1 further comprising] at least one switch assembly comprising a solid state switch, said at least one port controlling whether a respective said at least one solid state switch is in an open state or a closed state.